IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):	Steven E. Boor	**CONFIRMATION NO. 3966**
Appln No.:	10/797,507	
Filed:	March 10, 2004	This Response to Notification of Non-Compliant Appeal Brief was electronically filed on May 24, 2010 using the U.S. Patent and Trademark Office's EFS Web
Title:	MODIFIABLE BUFFER CIRCUIT FOR MINIATURE MICROPHONE APPLICATIONS AND METHOD OF ADJUSTING THEREOF	
Group Art Unit:	2614	
Examiner:	Olaniran, Fatimat O.	
Attorney Docket:	8354/96255	

Customer No.: 22242

RESPONSE TO SECOND NOTIFICATION OF NON-COMPLIANT APPEAL BRIEF

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to an Office Communication mailed May 4, 2010 as entered in the above-captioned matter, a Notice of Non-Compliant Appeal Brief has been entered. Box 4 of Form PTOL-462(rev.7-05) has been checked.

Box 4 indicates that (a) The Brief does not contain a concise explanation of the subject matter defined in each of the independent claims involved in the Appeal, referring to the Specification by page and line number and to the drawings, if any, by reference characters; and/or (b) the fails to: (1) identify, for each independent claim involved in the appeal and for each dependent claim argued separately, every means plus function and step plus function under 35 U.S.C. 112, sixth paragraph, and/or (2) set forth the structure, material, or acts described in the specification as corresponding to each claimed function with reference to the specification by page and line number, and to the drawings, if any, by reference characters.

As there are no means plus function or step plus function claims presented in this appeal and as confirmed by the explanation section of the Notice, section 4(b) is not at issue.

This Second Notice commented that "Specifically independent claims 1, 15, and 20 must be mapped to the specification as filed and not to the published specification, by page and line number." The applicant had responded to the First Notice using paragraph numbers of the published application.

The applicant below presents a concise explanation of the subject matter appears as follows in the form of claim subject matter maps with corresponding references to page and line numbers of the specification as filed and to the drawings by figure number and reference characters.[1]

Independent claim 1	*Fig. & Pg. & line of spec.*
A buffer circuit (100) for use in a microphone assembly (312) comprising:	FIG. 1, FIG. 5, and FIG. 6
an input (104) for receiving a signal;	FIG. 1 Specification, page 4, lines 13-23
an input buffer (102) coupled to the input (104);	FIG. 1 Specification, page 4, lines 13-23
an output (108);	FIG. 1 Specification, page 4, lines 13-23
a filter network (106) coupled between the input buffer (102) and	FIG. 1

1 As stated, there are no means plus function (or step plus function) recitations in any of the claims involved in this appeal, and therefore there is no identification of any corresponding structure, material, or acts in the specification in this regard. It will be understood that this summarization of the claimed subject matter is, in fact, a "summary" and that the applicant does not represent or intend that this brief presentation, or the accompanying references to the drawings and the specification, comprises an exhaustive presentation in this regard. As always, the claims are to be viewed and interpreted in view of the context of the entire specification sans the Abstract.

the output (108);	Specification, page 4, lines 13-23
a selector (112) comprising:	FIG. 1 Specification, page 4, lines 13-23
a first input (234);	FIG. 1, FIG. 4 Specification, page 4, lines 13-23; page 5, line 8-page 6, line 2
a first output (230) responsive to the first input (234); and	FIG. 1, FIG. 4 Specification, page 4, lines 13-23; page 5, line 8-page 6, line 2
a tuning circuit (110) coupled to the filter network (106) for adjusting a characteristic of the filter network, the tuning circuit (110) responsive to the selector (112), wherein the characteristic of the filer network (106) is adjusted using the first input (234).	FIG. 1, FIG. 4 Specification, page 4, lines 13-23; page 5, line 8-page 6, line 2

Independent claim 15 *Fig. & pg. & line of spec.*

A hybrid circuit for buffering an audio signal comprising:	
a substrate having a first and second portion (318), the second portion (318) severable from the first portion; and	FIG. 1, FIG. 2, FIG. 4, FIG. 5 Specification, page 4, lines 13-23; page 6, line 15- page 7, line 2; and page 8, lines 20-23
a buffer circuit (100) substantially disposed on the first portion of the substrate, the buffer circuit (100) comprising:	FIG. 1, FIG. 2, FIG. 4, FIG. 5 Specification, page 4,

	lines 13-23; page 6, line 15- page 7, line 2; and page 8, lines 20-23
a first input (234) for coupling the audio signal;	FIG. 1, FIG. 2, FIG. 4, FIG. 5 Specification, page 4, lines 13-23; page 5, line 8-page 6, line 2
a filter network (218) coupled to the first input (234);	FIG. 1, FIG. 2, FIG. 4, FIG. 5 Specification, page 4, lines 13-23; page 4, line 32-page 6, line 2
an output coupled to the filter network (218);	FIG. 1, FIG. 2, FIG. 4, FIG. 5 Specification, page 4, lines 13-23; page 5, line 8-page 6, line 2
a tuner (224) for adjusting the filter network (218); and	FIG. 1, FIG. 2, FIG. 3, FIG. 4, FIG. 5 Specification, page 4, lines 13-23; page 4, line 32-page 6, line 2
a controller (232) for altering a value of the tuner (224), the controller (232) having a second input (317), the second input (317) disposed on the second potion (318) of the substrate,	FIG. 1, FIG. 2, FIG. 3, FIG. 4, FIG. 5 Specification, page 4, lines 13-23; page 5, line 8-page 6, line 2
whereby a tuning signal coupled to the second input (317) is used to adjust the tuner (224), thereby changing a transfer function of the buffer circuit (100).	FIG. 1, FIG. 2, FIG. 3, FIG. 4, FIG. 5 Specification, page 4,

	lines 1-23; page 4, line 32-page 6, line 2

Independent claim 20 *Fig. & Pg.& line of spec.*

A method for adjusting a buffer circuit for use in a microphone assembly comprising:	FIG. 1
providing a desired response characteristic for the buffer circuit (100);	FIG. 1 Specification, page 4, lines 1-23; page 4, lines 24-31; page 6, line 15-page 7, line 2
Measuring an initial response characteristic of the buffer circuit (100);	FIG. 1 Specification, page 6, line 15- page 7, line 2
Comparing the desired response characteristic to the initial response characteristic;	FIG. 1 Specification, page 6, line 15- page 7, line 2
determining an adjustment using the comparison, the adjustment for reducing a difference between the desired and initial response characteristics;	FIG. 1 Specification, page 4, lines 1-31; page 6, line 15- page 7, line 13
transmitting a signal to a selector circuit in the buffer circuit (100); and	FIG. 1 Specification, page 7, lines 3-13
tuning an adjustable filter coupled to the selector circuit, the adjustable filter for modifying the initial response characteristic.	FIG. 1, FIG. 3 Specification, page 5, lines 8-20; page 7, lines 3-13

The applicant submits that these mappings bring the brief into compliance with the requirements of the Board. If the Examiner should have any other specific concerns, the Examiner is invited to contact the undersigned by telephone to seek an appropriate accommodation.

Respectfully submitted,

FITCH, EVEN, TABIN & FLANNERY

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